

IN THE CLAIMS

Please amend Claim 1 as follows:

1. A overcurrent protection circuit for a motor drive circuit, comprising:
- a first FET having a gate input and conducting a motor current of the motor, a drain and a source;
 - a second FET having a gate input coupled to said first FET input gate and conducting a bias current, a drain and a source; and
 - a comparator directly coupled across said drains of said first FET and said second FET and providing an output indicative of a voltage across said comparator inputs.
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